

FIG. 2A

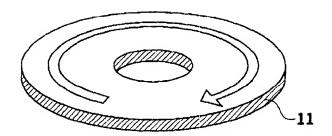


FIG. 2B

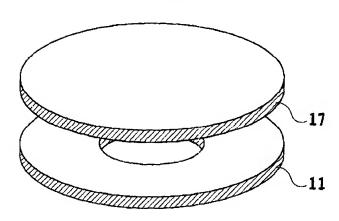
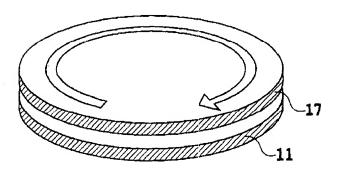
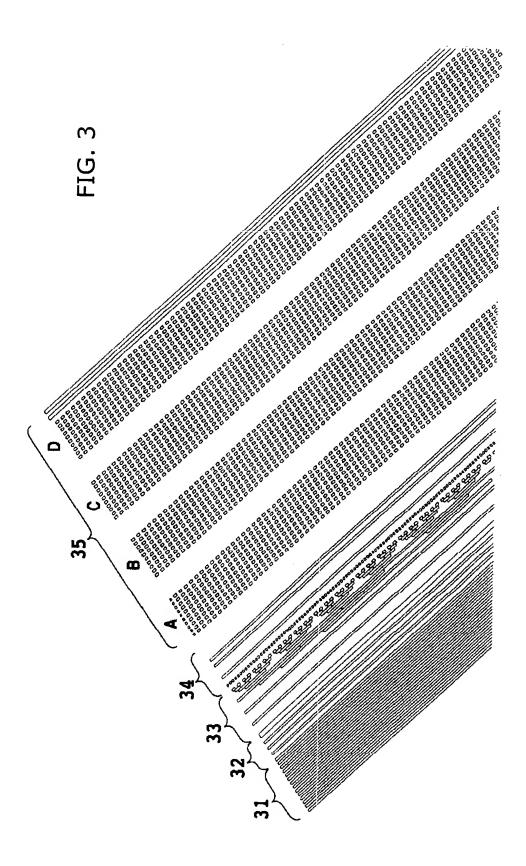


FIG. 2C





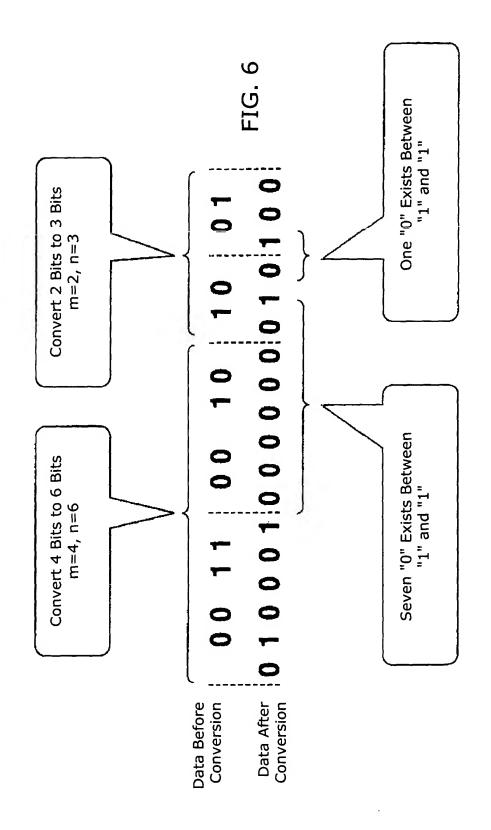
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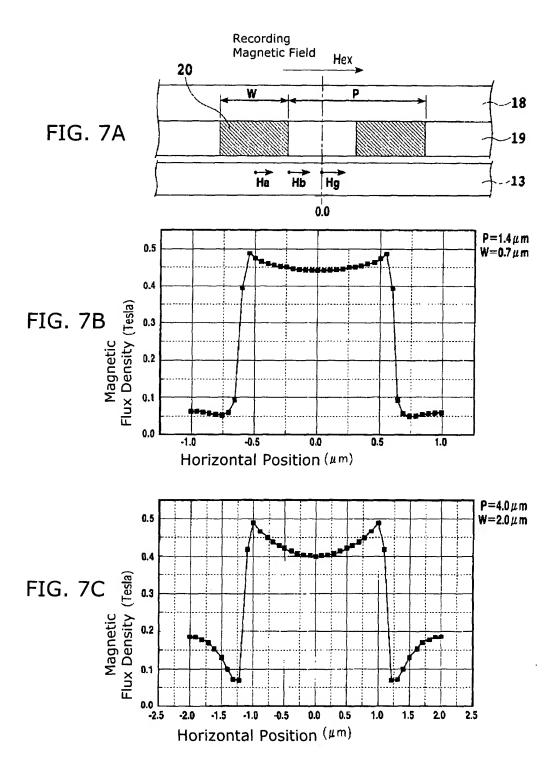
FIG. 4B

Data Before Moduation	Data After Modulation
01	X01
10	010
11	X011
0001	X00001
0010	X00000
0011	010001
0000	010000

 \boldsymbol{X} is 1 when the bit after modulation is 0

FIG. 5





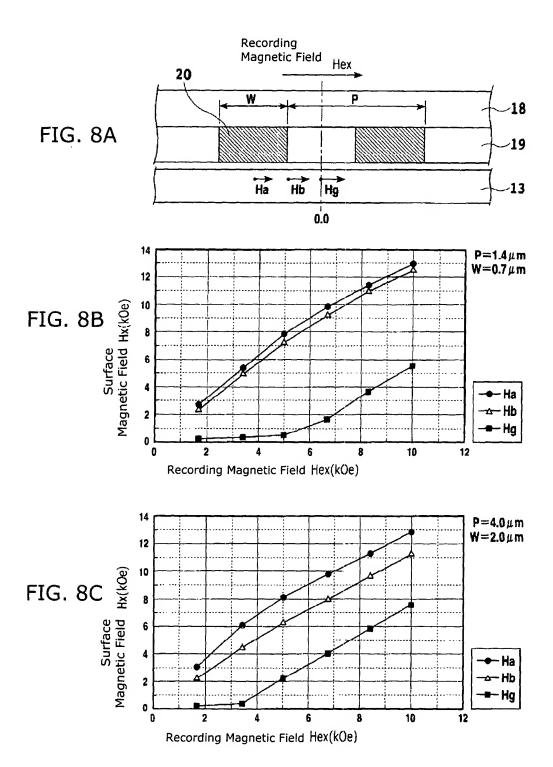


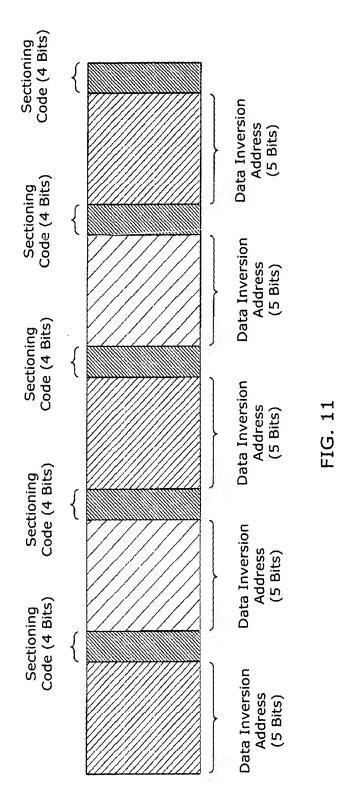


FIG. 9

Data Inversion Location

0 0

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18-bit Cylinder ID(gray coded)	00000000111111	1234567890123456	2 4 0	00000000000000000	00000000000000000	00000000000000000011	00000000000000000	00000000000000000	000000000000000011	0000000000000	000000000000000000000000000000000000000	000000000000000110	00000000000000110	0000000000000111	00000000000000111	000000000000000000	000000000000000000000000000000000000000	0000000000000100	000000000000000100	01001010000011	00100101000001	0001001010000010	001001010000110	0001001010000110
			CYLID	0	_	8	n	4	15	9	7	80	đ	10	-	<u>.</u>	13	14	15	247301	247302	247303	247304	247305



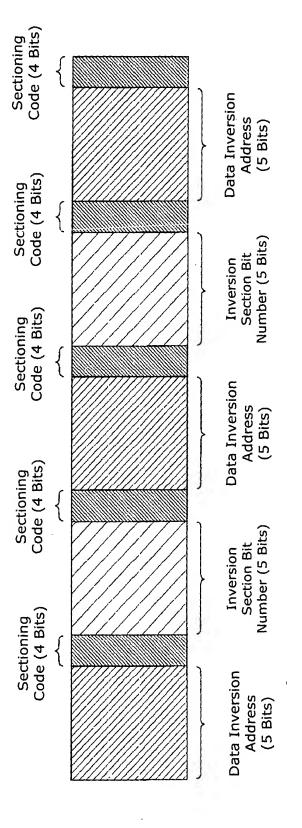


FIG. 12